

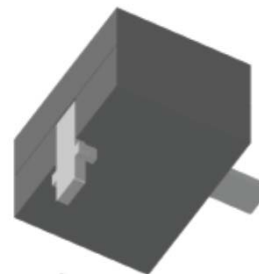


SLD422810 Series



1. Features:

- Ferrite based SMD Inductor with lower core loss.
- Custom values are welcomed.
- High current output chokes, upto 65.0 Amp with approx. 20% roll off.
- Low Profile 7.0mm Max. height .
- Foot Print 10.70 x 10.00 mm Max.
- Ideal for Buck Converter, VRM & High Density Board Design.
- Operating frequency up to 1 MHz application.
- Operating Temperature Range -55°C to + 130°C , RoHs & HF compliance .

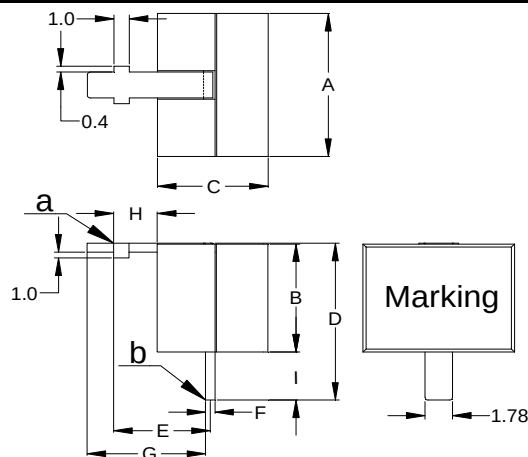


2. Electrical Characteristic of SLD422810 Series:

Part Number	OCL ¹ (nH) ±10%	DCR ³ (mΩ) ± 5.0%	L@Isat1 ² (nH) Min.	Isat1 ⁴ (A) @25°C	Isat2 ⁴ (A) @75°C	Isat3 ⁴ (A) @100°C	Isat4 ⁴ (A) @125°C	Irms ⁵ (A) @25°C
SLD422810A-R135KHF	135.0	0.245	90.00	65.00	58.00	55.00	53.00	46.00

3. Mechanical Dimension(Unit:mm):

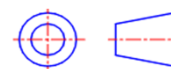
A	B	C	D	E	F	G	H	I
Max.	Max.	Max.	±0.20	±0.20	Nom.	±0.20	Min.	Min.
10.00	7.75	7.00	10.50	5.25	0.60	7.35	1.55	2.65



Note:

- 1>.Open Circuit Inductance (OCL) test condition:500KHz,0.25Vrms,0Adc ,at 25°C.
- 2>.L @ Isat and L @ Irms Test condition:500KHz,0.25Vrms ; (Ta=25°C).
- 3>.The nominal DCR is measured from point "a" to point "b",as shown above on the mechanical drawing.
- 4>.Isat1,Isat2,Isat3 & Isat4 : DC current that will cause inductance to drops approximately by 20% ;
- 5>. Irms: DC current for an approximate temperature rise of 40°C without core loss,.Derating is necessary for AC currents. PCB pad layout,trace thickness and width,air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130°C under worst case operating conditions verified in the end application.

Third Angle Projection:



4. Inductance Characteristics (Inductance vs. Current):

