

1. Features of AHA40475A Series :

- Ferrite based TLVR inductor with lower core loss.
- Inductance Range: 70.0nH to 150.0nH, Custom values are welcomed.
- High current output chokes, up to 135.0 Amp with approx. 20% roll off.
- Low Profile 12.00 mm Max. height .
- Foot Print 10.00 x 5.00 mm .
- Perfect for high density designs with limited board space.
- Operating frequency up to 5.0 MHz application.
- Operating Temperature Range -55° C to +130° C , RoHs & HF compliance .
- T & R Qtys: 300 pcs , 13" Reel ;



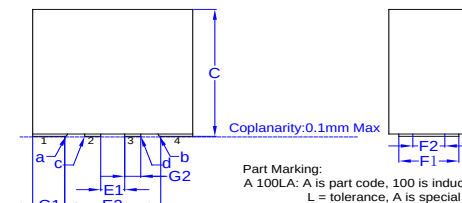
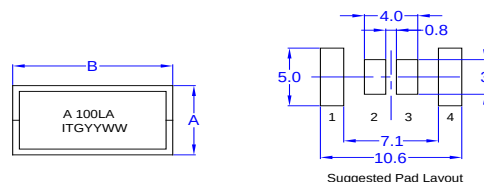
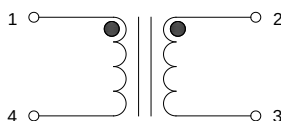
2. Electrical Characteristic of AHA40475A Series:

ITG Part Number	OCL ¹	L @ Isat1 ²	DCR1 ³	DCR2 ³	Isat1 ⁴	Isat2 ⁴	Isat3 ⁴	Irms1 ⁵	Irms2 ⁵
	Pin 1-4 & 2-3	Pin 1-4 & 2-3	Pin 1-4	Pin 2-3				Pin 1-4	Pin 2-3
	(nH) ± 15%	(nH) Min.	(mΩ) ± 10%	(mΩ) ± 10%	(A) @25°C	(A) @75°C	(A) @100°C	(A) @25°C	(A) @25°C
AHA40475A-70L	70.00	50.40	0.16	0.60	135.00	125.00	116.00	64.00	35.00
AHA40475A-80L	80.00	57.60	0.16	0.60	120.00	108.00	100.00	64.00	35.00
AHA40475A-90L	90.00	64.80	0.16	0.60	108.00	95.00	90.00	64.00	35.00
AHA40475A-100L	100.00	72.00	0.16	0.60	98.00	90.00	82.00	64.00	35.00
AHA40475A-120L	120.00	86.40	0.16	0.60	81.00	75.00	69.00	64.00	35.00
AHA40475A-150L	150.00	108.00	0.16	0.60	64.00	59.00	55.00	64.00	35.00

3. Mechanical Dimension(Unit : mm):

A	B	C	E1	E2	F(Pin1&4)	F(Pin2&3)
Max.	Max.	Max.	± 0.20	± 0.20	± 0.20	± 0.20
5.00	10.00	12.00	1.30	7.60	4.50	2.40
G1	G2					
± 0.20	± 0.20					
1.10	1.10					

Schematic:



Part Marking:
A 100LA: A is part code, 100 is inductance value in nH,
L = tolerance, A is special code.
ITGYYWW: ITG logo and YYWW is date code.

Third Angle Projection:



Notes:

1. Open Circuit Inductance (OCL) test condition:500KHz,0.25Vrms,0Adc ,at 25 °C.
2. L @ Isat and L @ Irms Test condition:500KHz,0.25Vrms (Ta=25 °C).
3. The nominal DCR1 is measured from point "a" to point "b", as shown above on the mechanical drawing (Ta=25°C).
The nominal DCR2 is measured from point "c" to point "d", as shown above on the mechanical drawing (Ta=25°C).
4. Isat1,Isat2 & Isat3 for OCL Pin 1-4 or 2-3: DC current that will cause inductance to drop approximately by 20%.
5. Irms: DC current for an approximate temperature rise of 40°C without core loss , Derating is necessary for AC currents.
PCB pad layout , trace thickness and width , air-flow and proximity of other heat generating components will affect the temperature rise. It is recommended the part temperature not exceed 130 °C under worst case operating conditions verified in the end application.
6. High pot: 100Vdc, 1s, 3mA.

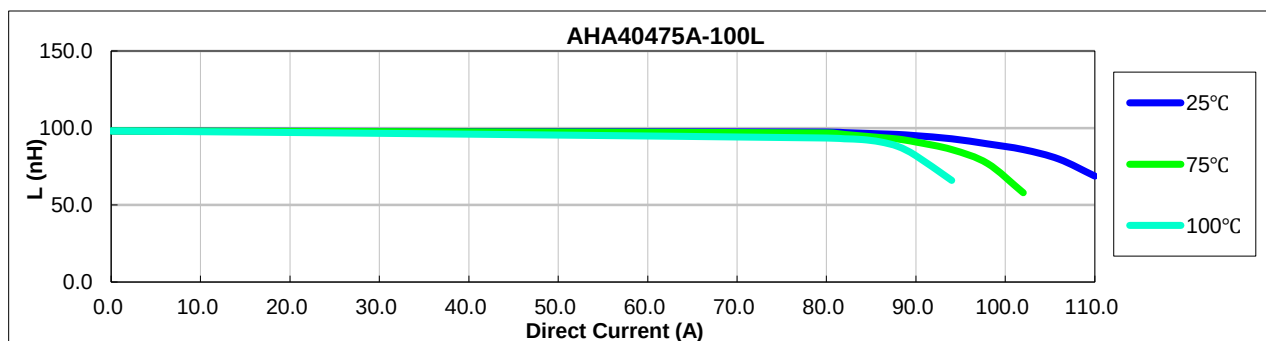
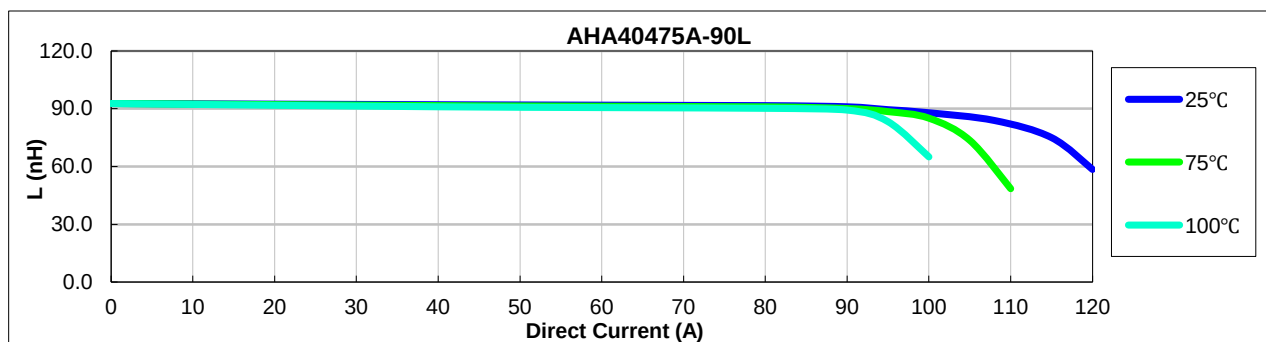
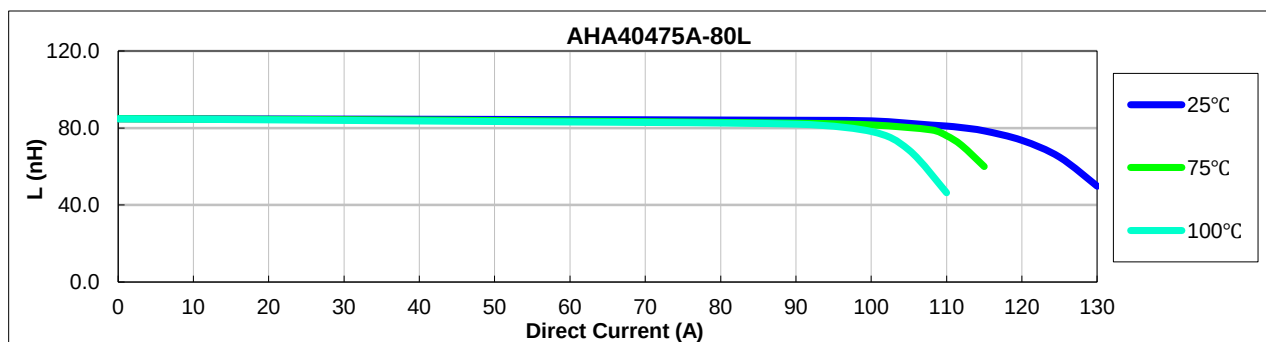
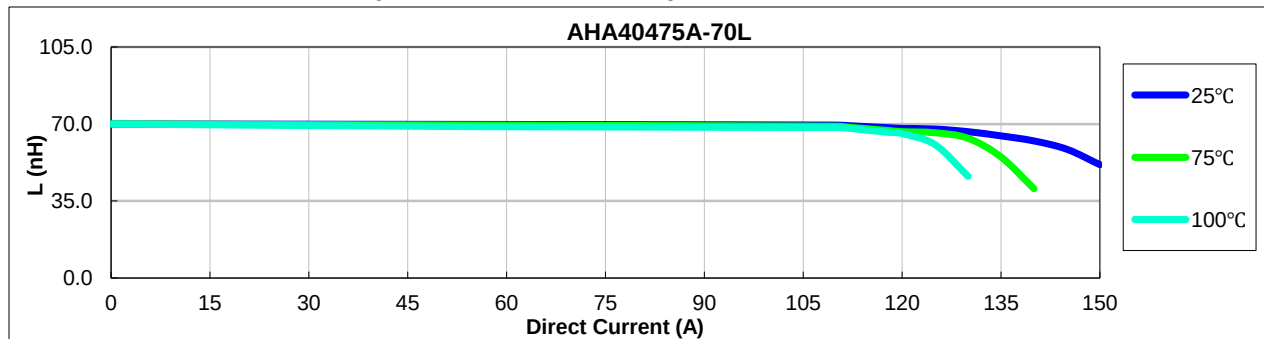
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*Due to continuous product improvement, all specifications are subject to change without prior notice. Kindly contact an ITG field application engineer or a sales representative prior to purchase.

4. Inductance Characteristics (Inductance vs. Current):



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